Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application. Please cancel claim 1, and add new claims 52-82, as follows:

Listing of Claims:

1-51. (Cancelled)

52. (New) A method of generating expect signals for synchronizing a memory bus of a memory device to which synchronizing signals are applied, the method comprising:

receiving on the bus a plurality of synchronizing signals representing a stream of binary digits;

capturing a group of the synchronizing signals; and

generating a plurality of expect signals based on the binary digits represented by the captured signals for comparison to the synchronizing signals received subsequent to the captured group of synchronizing signals.

53. (New) The method of claim 52 wherein capturing the group of the synchronizing signals comprises capturing a group of the synchronizing signals not including the first of the synchronizing signals.

54. (New) The method of claim 52 wherein receiving the plurality of synchronizing signals comprises receiving a plurality of synchronizing signals representative of a pseudo-random sequence of binary digits.

55. (New) The method of claims 54 wherein the plurality of synchronizing signals comprises the repeating bit sequence of "111101011001000."

56.   (New) The method of claim 54 wherein generating the plurality of expect signals based on the bits represented by the captured signals comprises generating a plurality of expect signals corresponding to a stream of binary digits having the pseudo-random sequence of binary digits continuing from the sequence of binary digits of the captured group of the synchronizing signals.

57.   (New) The method of claim 52 wherein capturing a group of the synchronizing signals comprises capturing a group of the synchronizing signals corresponding to four binary digits.

58.   (New) The method of claim 57 wherein receiving the plurality of synchronizing signals comprises receiving a plurality of synchronizing signals representative of a repeating 15-bit pseudo-random sequence of binary digits.

59.   (New) A method for generating expect signals in response to receiving digital signals representative of a repeating sequence of digital signals on a memory bus provided for synchronizing the memory bus, the method comprising:
        capturing a group of digital signals of the repeating sequence of digital signals; and
        based on the bit sequence of the captured group of digital signals, generating in response thereto expect signals representing a bit stream of expect data to which the synchronizing signals are compared.

60.   (New) The method of claim 59 wherein the repeating sequence of digital signals comprises a pseudo-random sequence of binary digits.

61.   (New) The method of claim 60 wherein the pseudo-random sequence of binary digits comprises the repeating bit sequence of "111101011001000."

62.   (New) The method of claim 59 wherein capturing a group of digital signals comprises capturing a group of digital signals representative of four bits.

63.   (New) The method of claim 62 wherein the stream of binary digits having the pseudo-random sequence of binary digits comprises a 15-bit pseudo-random sequence of binary digits.

64.   (New) The method of claim 59 wherein the captured group of digital signals does not include the digital signal representing the first digit of the sequence of digital signals.

65.   (New) The method of claim 64 wherein the repeating sequence of digital signals comprises a repeating 15-bit pseudo-random sequence of binary digits.

66.   (New) The method of claim 59 wherein the repeating sequence of digital signals comprises repeating a pseudo-random sequence of binary digits and generating expect signals representing a bit stream of expected data comprises generating expect signals representing a stream of bits having the pseudo-random sequence following the sequence of the captured group of digital signals.

67.   (New) A method for generating an expect data pattern from an applied bit stream having a known pattern, comprising capturing a seed group of bits from the applied bit stream, and generating an expected group of bits having values determined by the values of the captured seed group of bits.

68.   (New) The method of claim 67 wherein the applied bit stream comprises a 15-bit pseudo-random bit sequence.

69.   (New) The method of claim 68 wherein generating an expected group of bits comprises generating a stream of bits starting with the sequence of bits of the seed group and continuing the pseudo-random bit sequence thereafter.

70.   (New) The method of claim 68 wherein the 15 bit pseudo-random bit sequence comprises the repeating bit sequence of "111101011001000."

71.   (New) The method of claim 67 wherein the captured seed group of bits includes 4 bits.

72.   (New) The method of claim 67 wherein the seed group of bits captured does not include the first bit of the applied bit stream.

73.   (New) A method of generating a sequence of digital signals for synchronizing a bus of a memory device, the method comprising:

repetitively applying digital signals to the bus in a known repeating sequence;

storing a group of digital signals; and

generating a series of expect data groups, each expect data group including a plurality of expect digital signals having values determined in response to the values of expect digital signals in the preceding expect data group, and the values of the digital signals in the first expect data group being determined responsive to digital signals from one of the captured groups of digital signals.

74.   (New) The method of claim 73 wherein repetitively applying digital signals comprises applying a repeating 15 bit pseudo-random bit sequence of digital signals.

75.   (New) The method of claim 74 wherein the 15 bit pseudo-random bit sequence comprises the repeating bit sequence of "111101011001000."

76.   (New) The method of claim 73 wherein each group comprises four digital signals.

77.   (New) The method of claim 73 wherein the group of digital signals stored does not include the first bit of the applied bit stream.

78.   (New) The method of claim 73 wherein generating a series of expect data groups comprises generating a stream of bits having the known repeating sequence beginning with the sequence of the stored group of digital signals.

79.   (New) A method for generating a sequence of expected data for use in synchronizing a memory bus, the method comprising:

applying a repeating pseudo-random sequence of binary digits to the bus;

selecting a 4-bit group of the pseudo-random sequence; and

based on the bit sequence of the 4-bit group, generating the repeating pseudo-random sequence of binary digits beginning with the four bits of the 4-bit group and continuing the pseudo-random sequence therefrom.

80.   (New) The method of claim 79 wherein applying a repeating pseudo-random sequence of binary digits comprises applying a 15-bit repeating pseudo-random sequence of binary digits.

81.   (New) The method of claim 80 wherein the 15-bit repeating pseudo-random sequence of binary digits comprises the repeating bit sequence of "111101011001000."

82.   (New) The method of claim 79 wherein the selected 4-bit group does not include the first bit of the repeating pseudo-random sequence of binary digits.